

FIG. 1 PRIOR ART

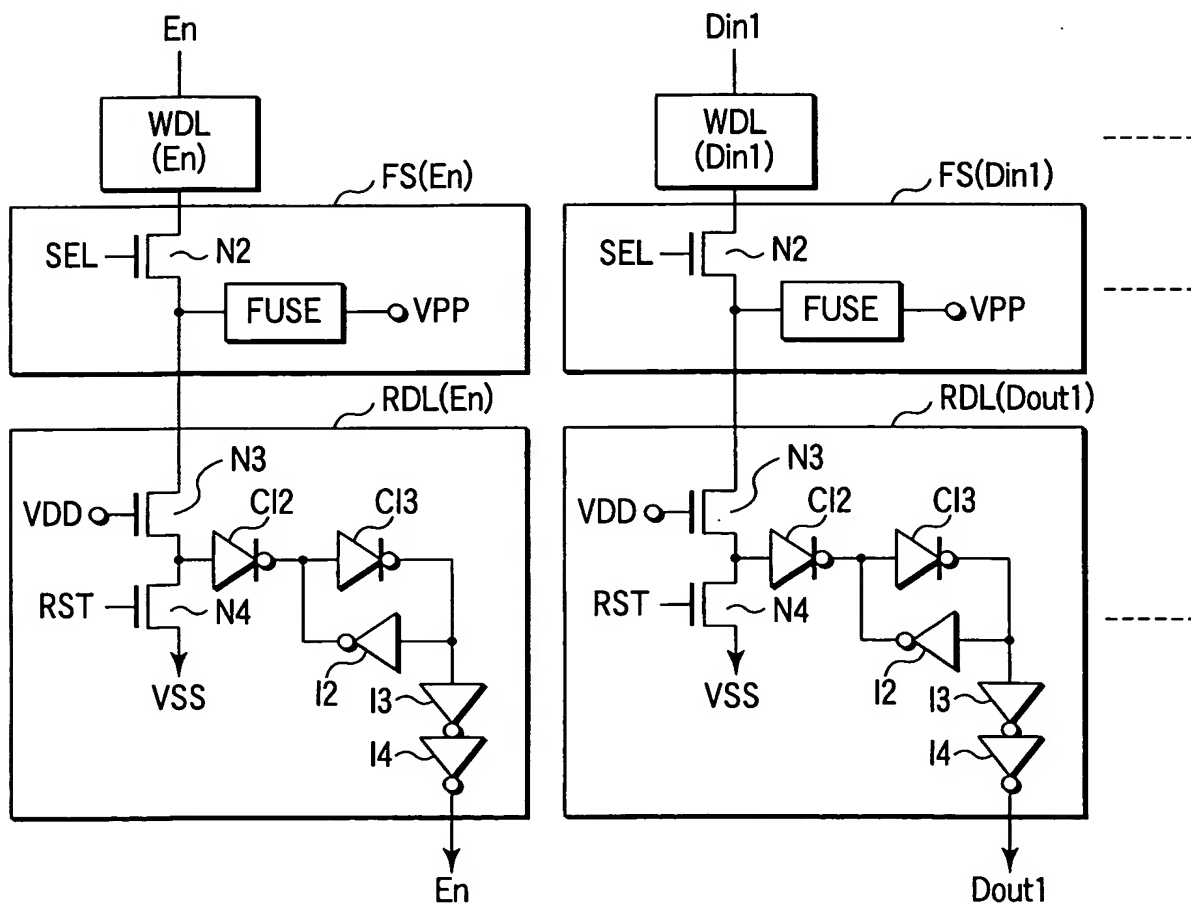


FIG. 2 PRIOR ART

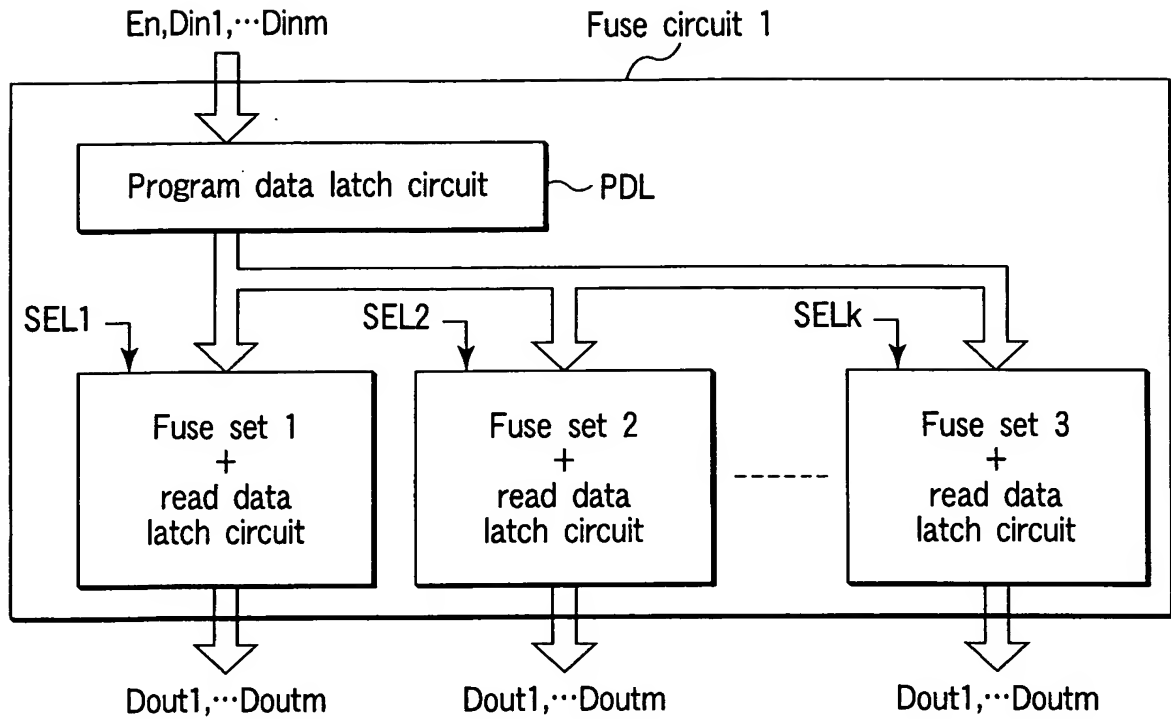


FIG. 3

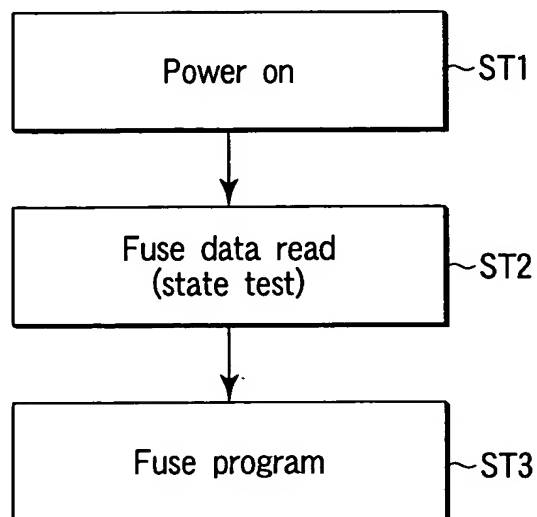


FIG. 8

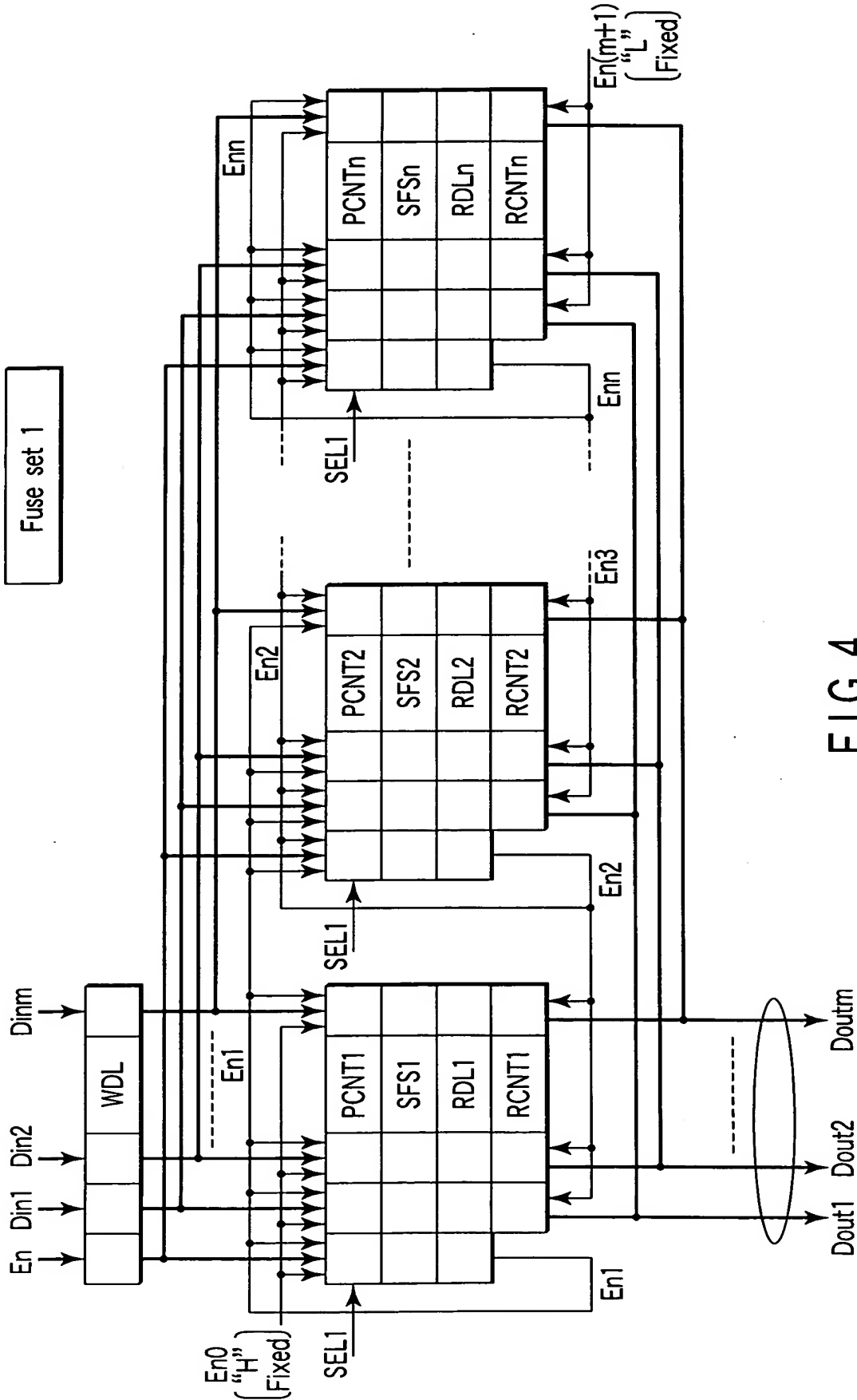


FIG. 4

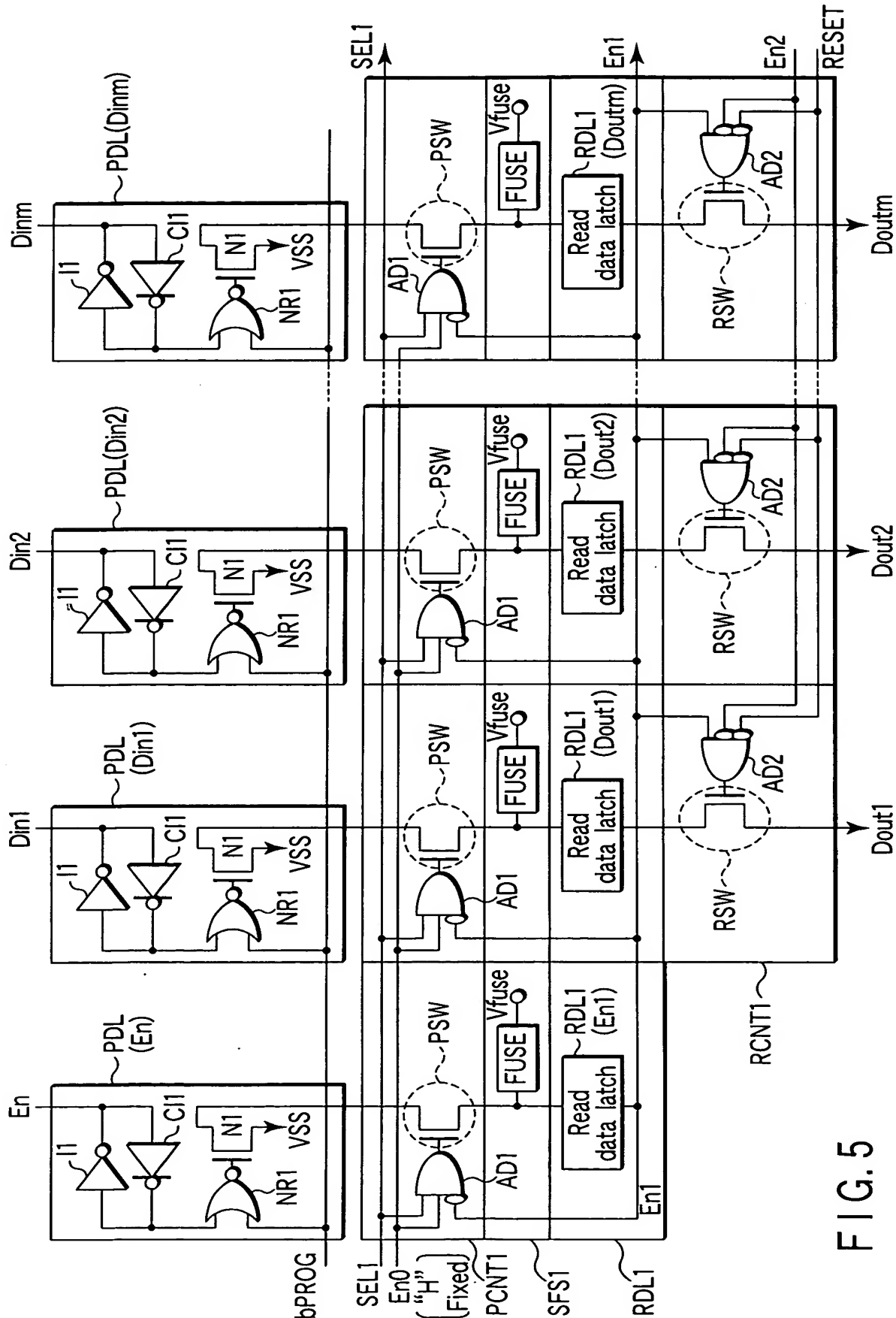
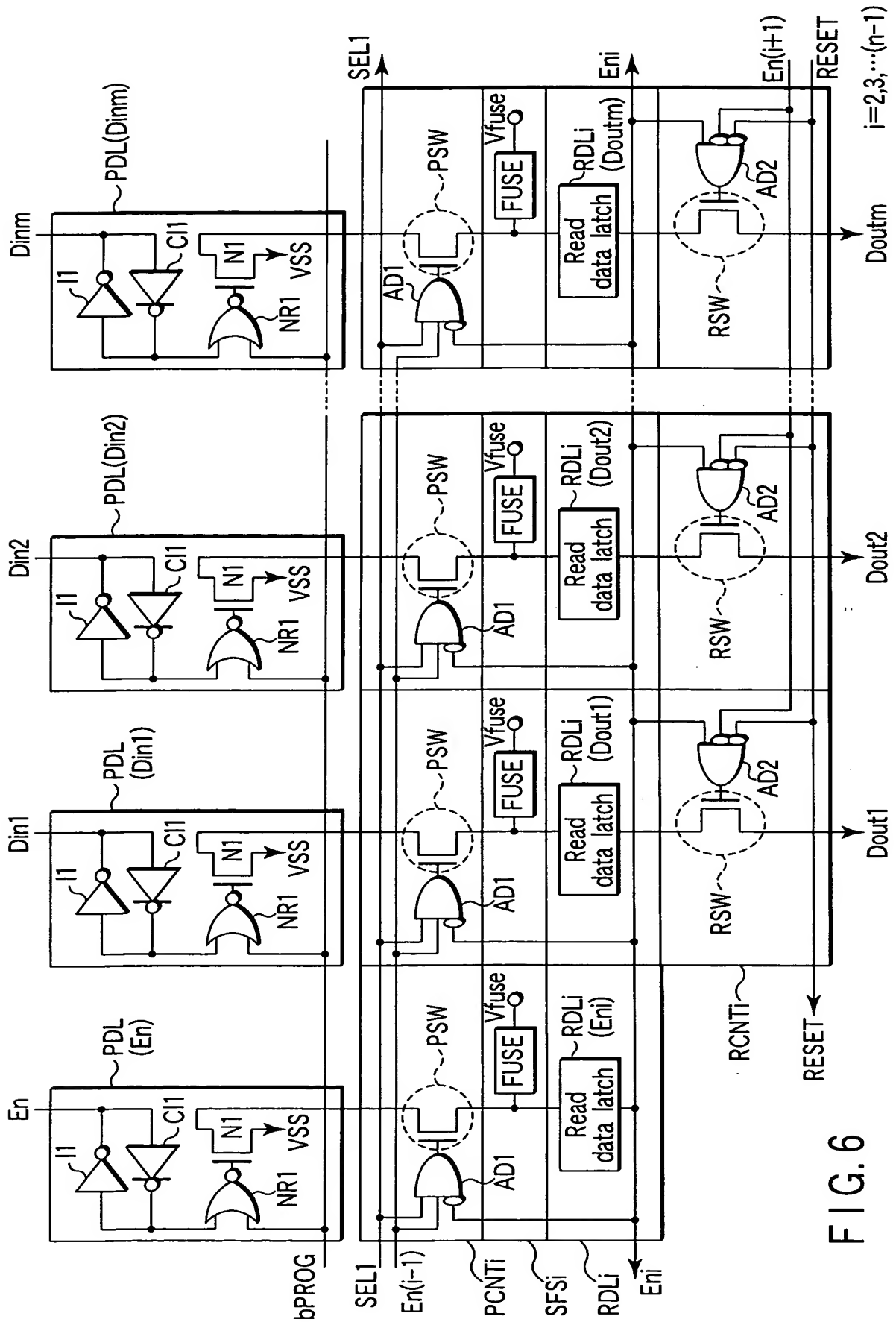


FIG. 5



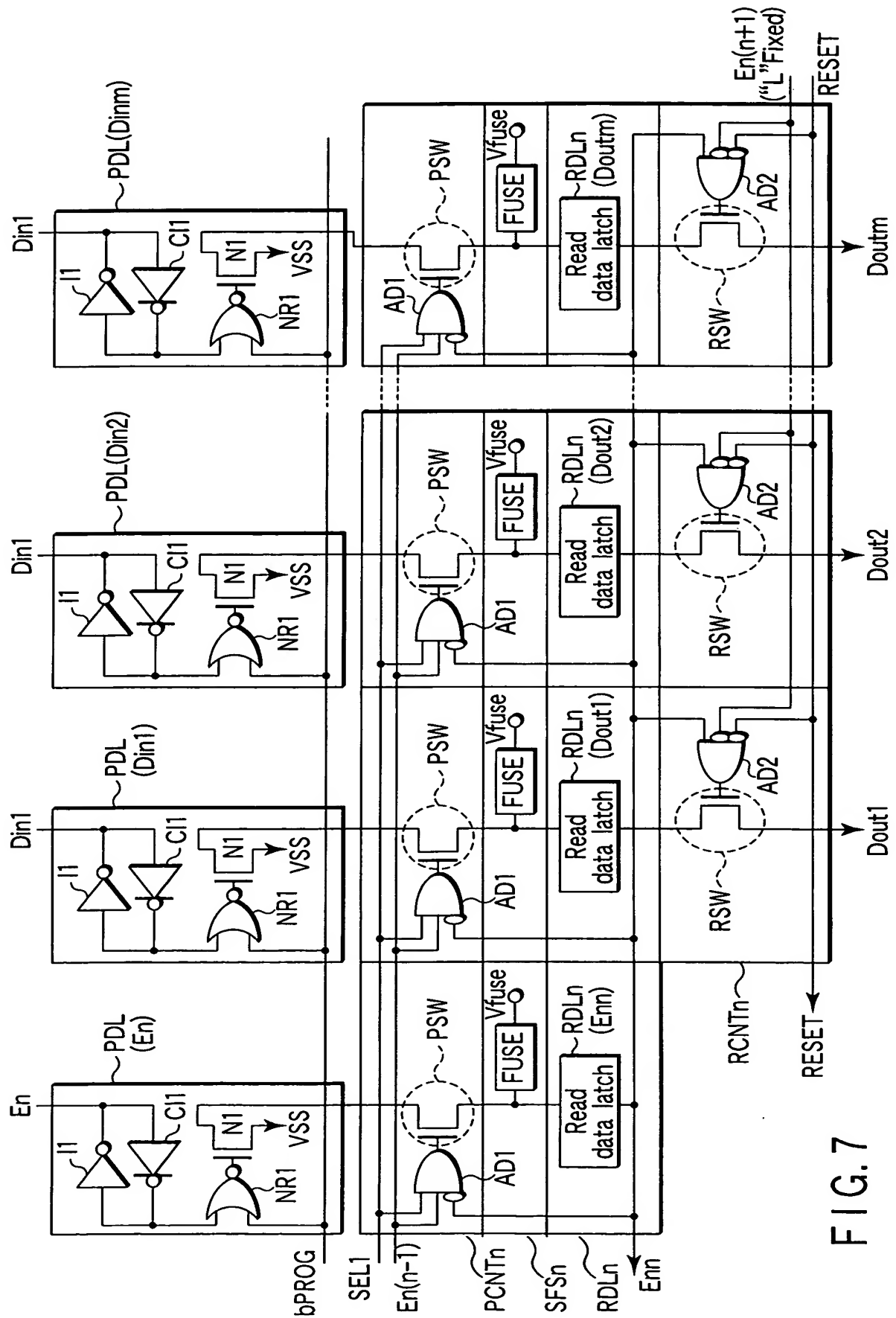


FIG. 7

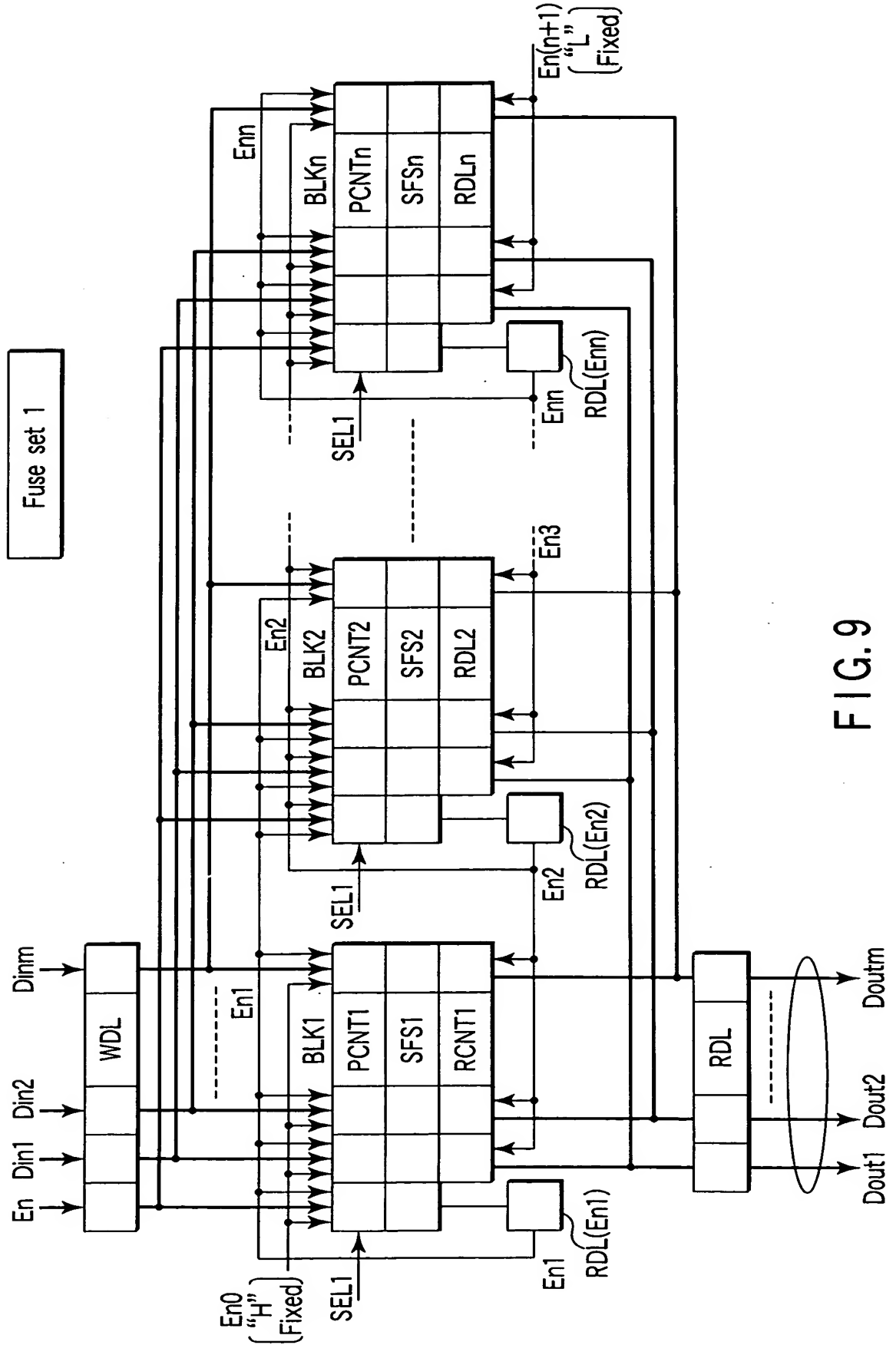


FIG. 9





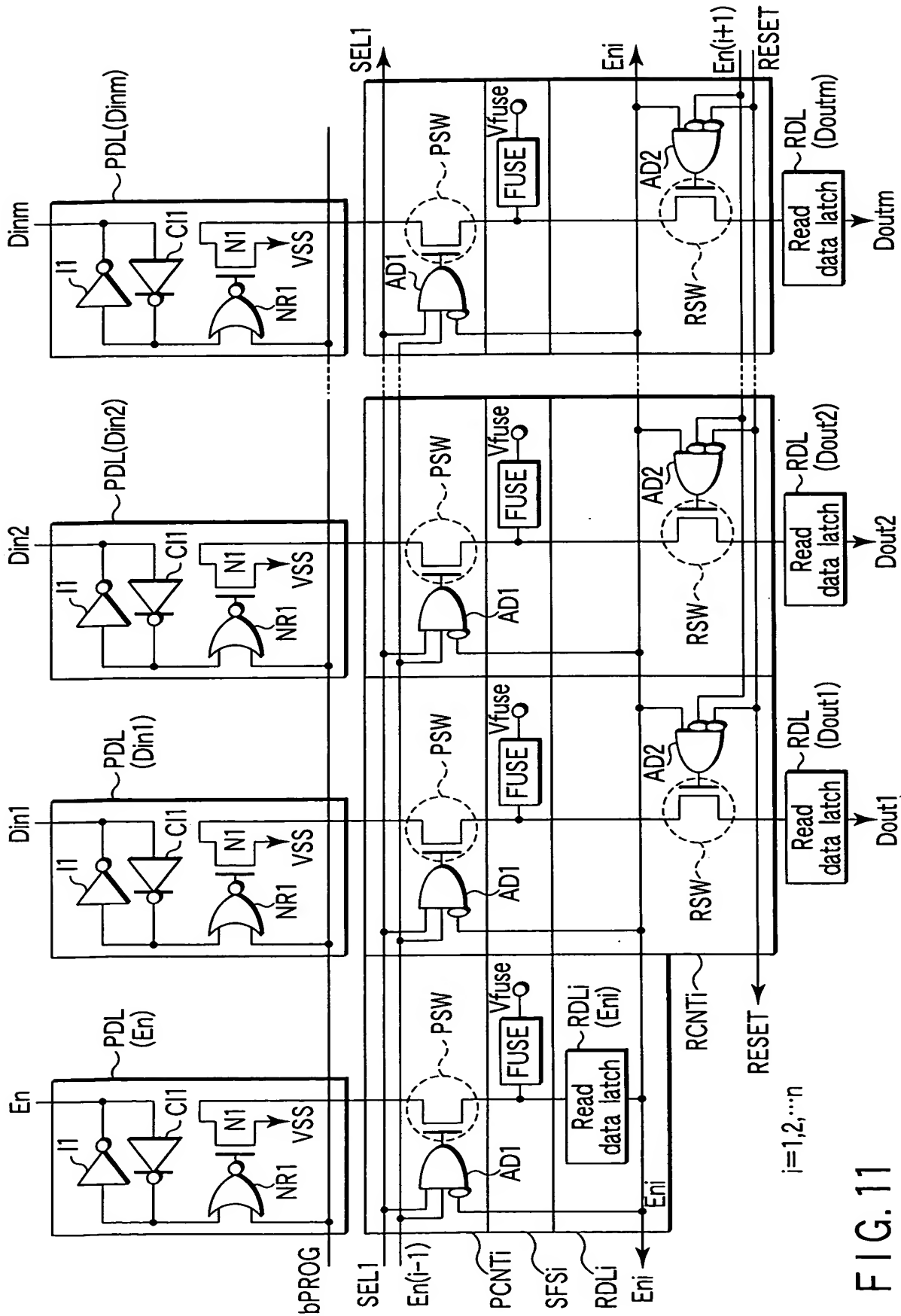


FIG. 11

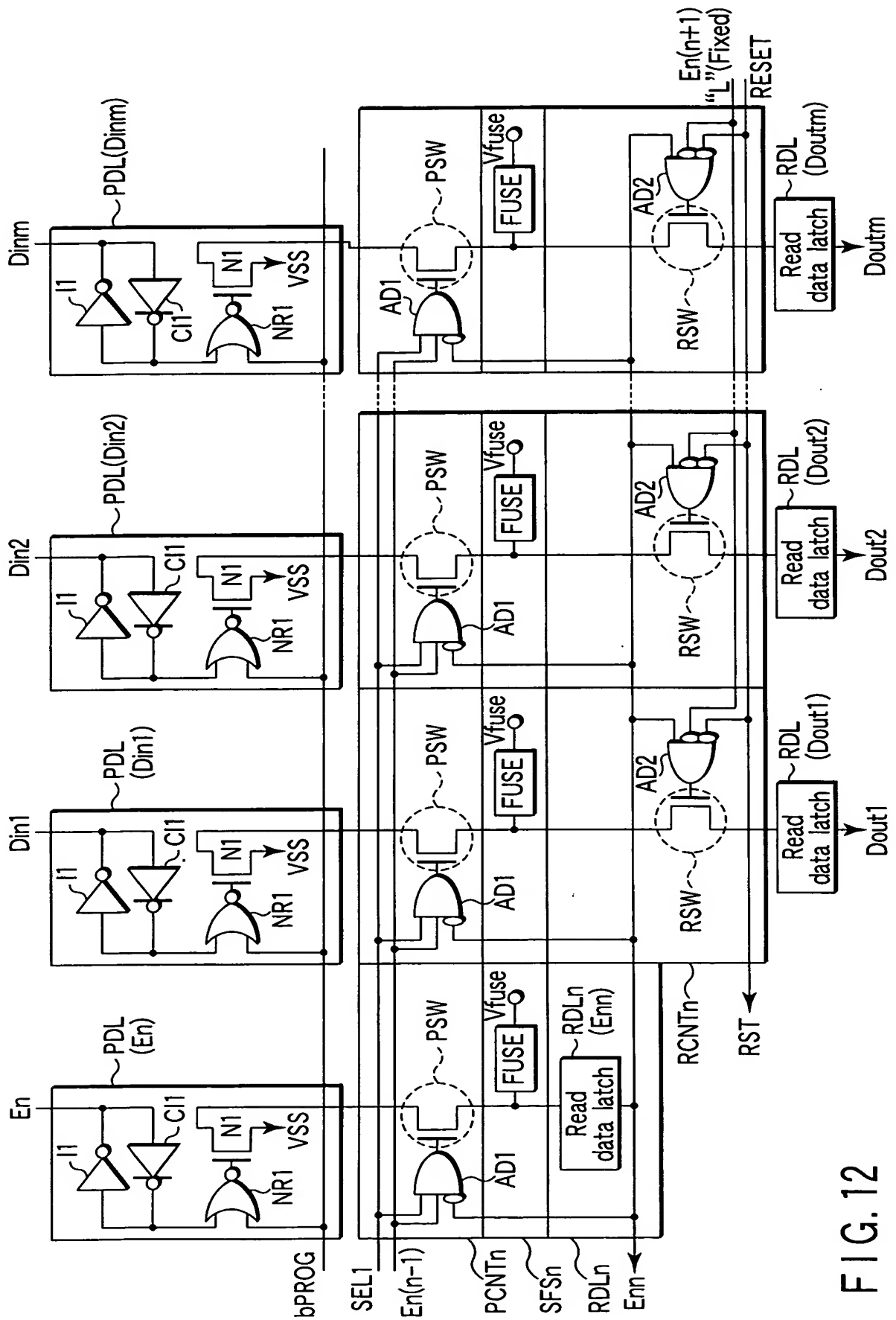
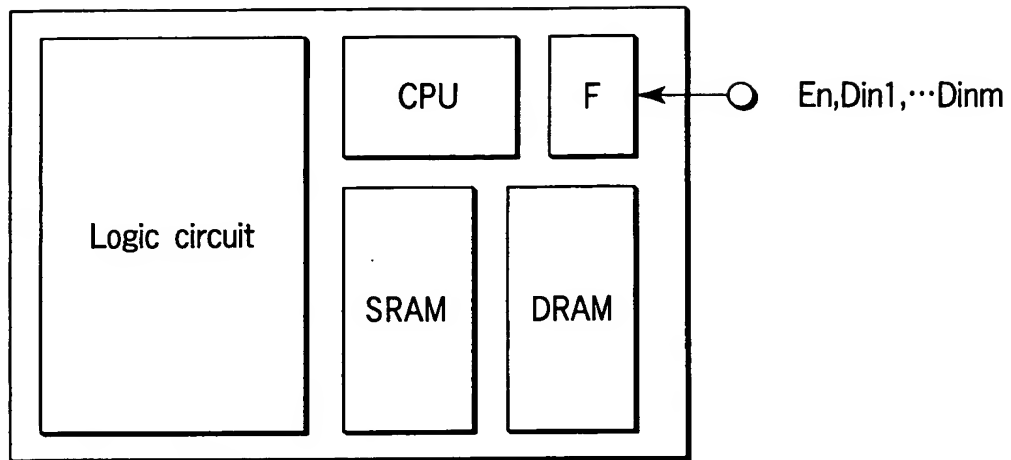
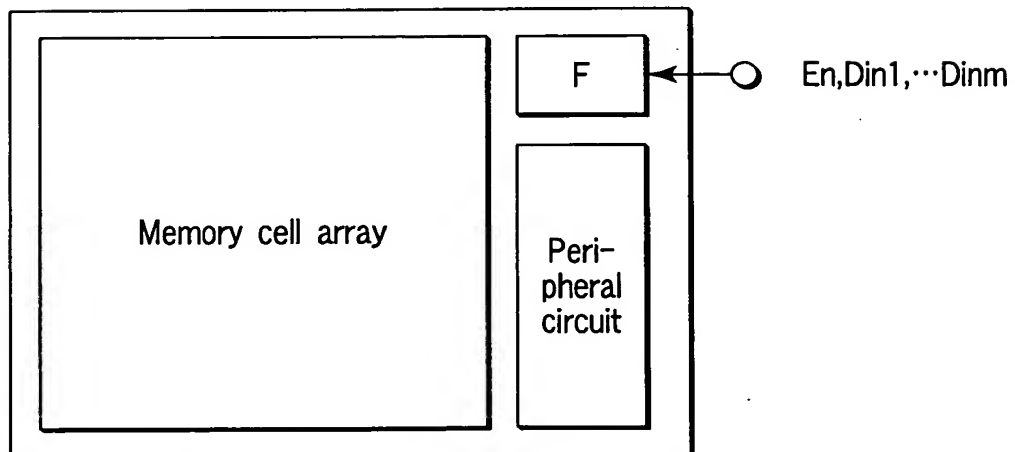


FIG. 12



F : Fuse circuit of the present invention

FIG. 13



F : Fuse circuit of the present invention

FIG. 14

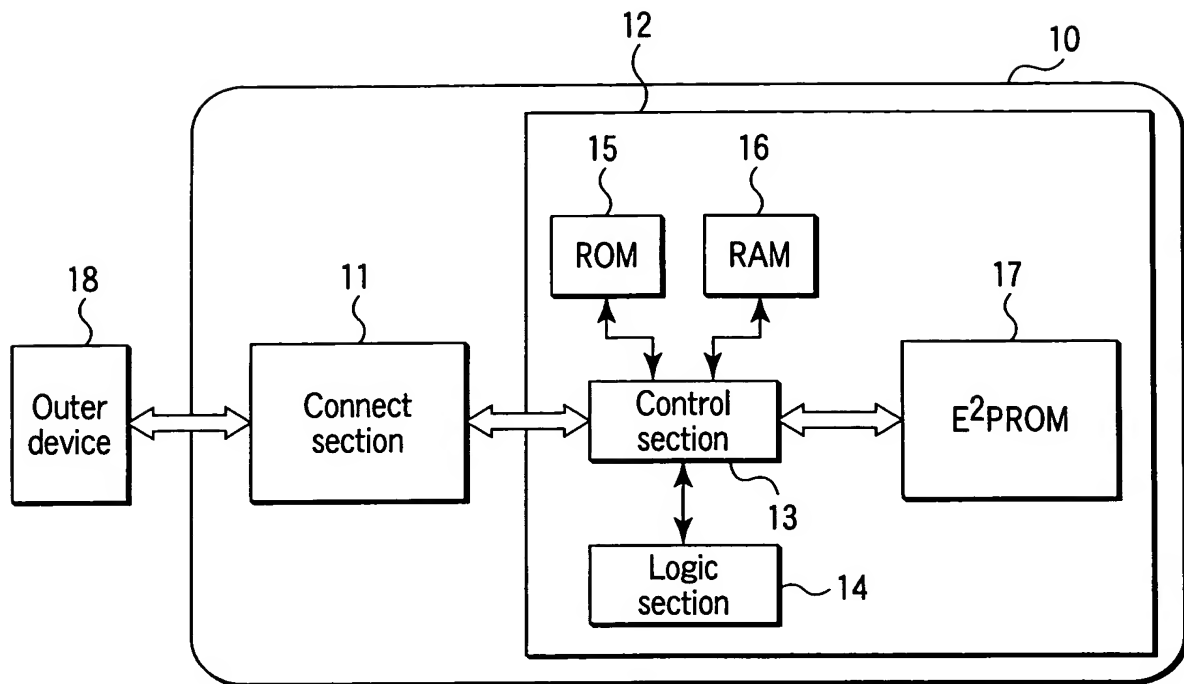


FIG. 15

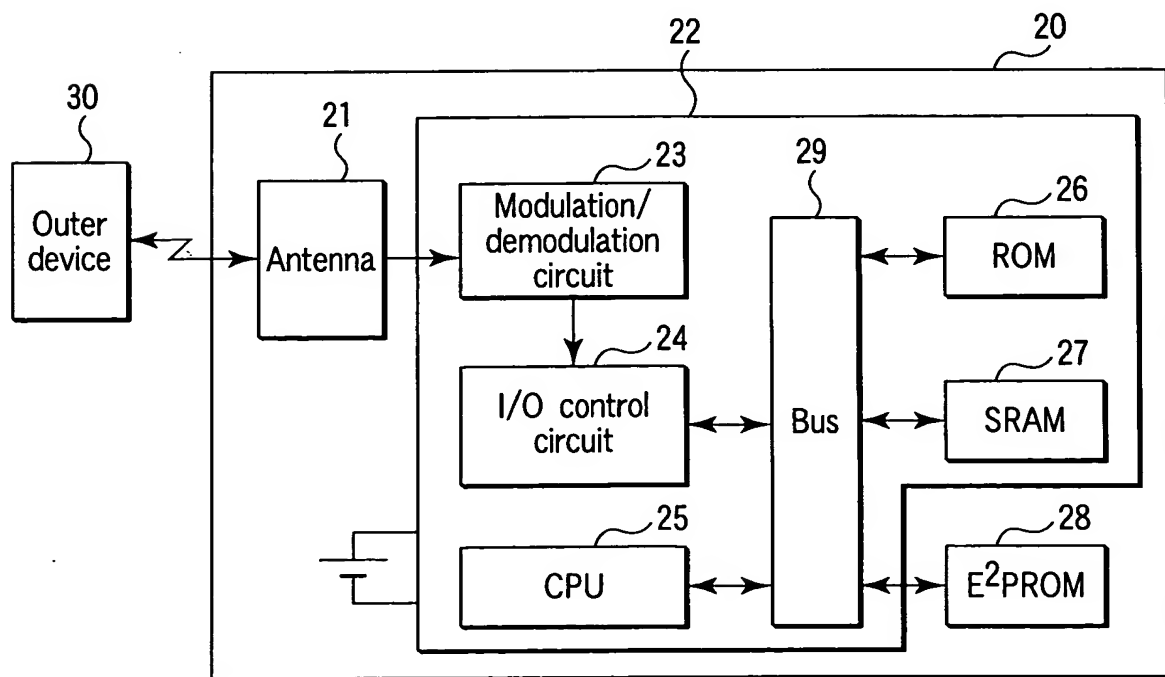


FIG. 16

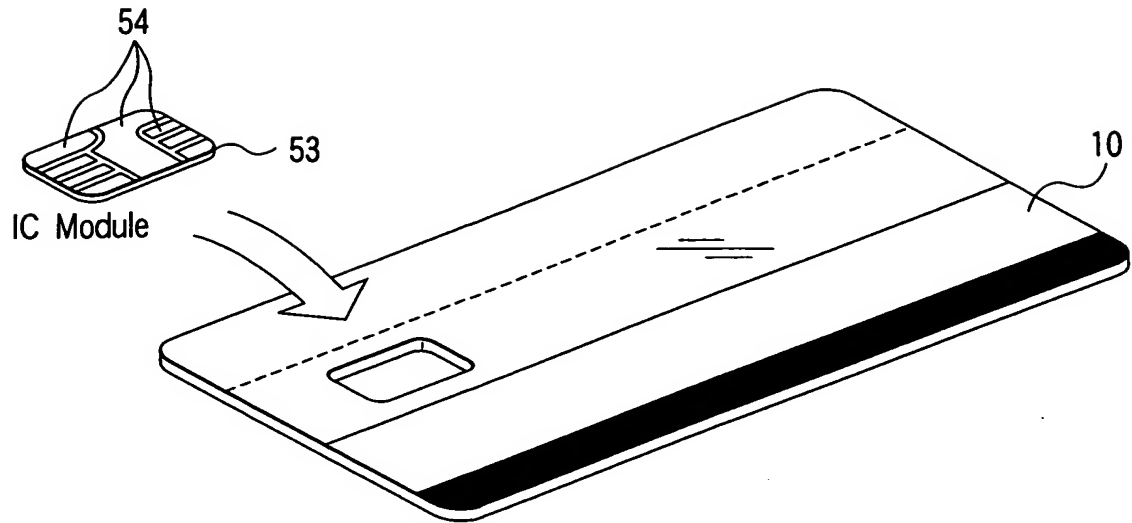


FIG. 17

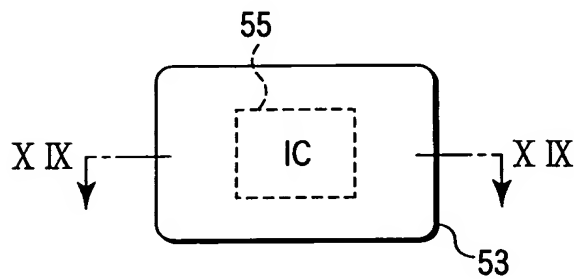


FIG. 18

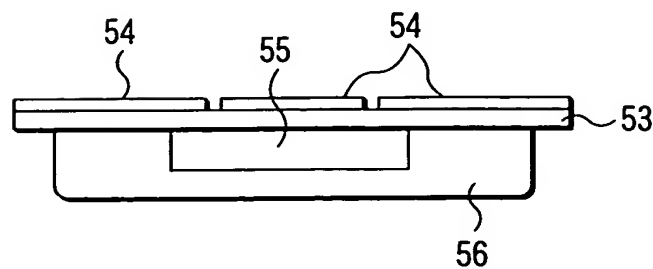


FIG. 19